

Attorney's Docket No. 354096.01201 - PATENT  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Lester J. Kozlowski; Markus Loose

Serial No.: Not Known

Filed: Herewith

For: CMOS IMAGING SYSTEM WITH LOW FIXED  
PATTERN NOISE

Examiner: Unknown

Group Art Unit: Unknown

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**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner of Patents and Trademarks  
Alexandria, VA 22313-1450

Sir:

Applicant submits herewith patents, publications or other information (listed on the attached Form PTO/SB/08A) of which Applicant is aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR §1.56.

In accordance with 37 CFR §1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that the information cited in this statement is, or is considered to be, material to patentability as defined in 37 CFR §1.56(b).

In accordance with 37 CFR §1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR §1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR §1.98 and MPEP §609 and the Examiner is respectfully requested to consider the listed references.

Respectfully submitted,

REED SMITH LLP

Dated: February 11, 2004

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Attached: Form PTO-1449

FORM PTO-1449 (Rev. 7-80)		U.S. Dept. of Commerce Patent and Trademark Office		Atty Docket No. 354096.01201		Serial No.: Not Known		
<b>INFORMATION DISCLOSURE CITATION</b>  (Use several sheets if necessary)				Applicant: Lester J. Kozlowski, Markus Loose				
				Filing Date: Herewith		Group: Not Known		
<b>U.S. PATENT DOCUMENTS</b>								
*Examine r Initials	AA	Document Number	Date	Name	Class	Subclass	Filing Date	
	AA	5,382,977	01/17/95	Kozlowski, et al			09/21/93	
	AB	5,892,540	04/06/99	Kozlowski, et al			06/13/96	
	AC	5,929,434	07/27/99	Kozlowski, et al			08/13/97	
	AD	6,417,504	07/09/02	Kozlowski			09/29/00	
	AE	6,456,326	09/24/02	Fossum, et al			01/02/01	
	AF	6,476,374	11/05/02	Kozlowski, et al			04/25/00	
	AG	6,483,116	11/19/02	Kozlowski, et al			09/25/00	
	AH	6,493,030	12/10/02	Kozlowski, et al			04/08/98	
	AI	6,498,331	12/24/02	Kozlowski, et al			12/21/99	
	AJ	6,504,141	01/07/03	Kozlowski, et al			09/27/00	
	AK	6,532,040	03/11/03	Kozlowski, et al			09/09/98	
	AL	6,535,247	03/18/03	Kozlowski, et al			05/19/98	
	AM	6,538,245	03/25/03	Kozlowski			10/26/00	
	AN	6,566,697	05/20/03	Fox, et al			01/15/02	
	AO	6,587,142	07/01/03	Kozlowski, et al			10/01/98	
	AP	5,471,515	11/28/95	Fossum, et al			01/28/94	
	AQ	6,624,850	09/23/03	Guidash			12/30/98	
	AR	5,880,460	03/09/99	Merrill			06/09/97	
	AS	6,115,066	09/05/00	Gowda, et al			06/12/97	
	AT	6,583,817	06/24/03	Lee			06/24/98	
	AU	5,248,971	09/28/93	Mandl			05/19/92	
<b>FOREIGN PATENT DOCUMENTS</b>								
		Document Number	Date	Country	Class	Subclass	TraNslation	
							Yes	No
								X
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)								

	AV	Youxin Gao; D.F. Wong, "Wire-sizing for Delay Minimization and Ringing Control Using Transmission Line Model" Department of Computer Sciences, University of Texas at Austin, Austin, Texas 78712
	AW	K. Chen, M. Afghahi, P.E. Danielsson, C. Svensson "PASIC: A Processor-A/D converter-Sensor Integrated Circuit"; 1990 IEEE, pages 1705-1708
	AX	Keping Chen, Per-Erik Danielsson, Anders Astrom "PASIC A Sensor/Processor Array For Computer Vision"; 1990 IEEE, pages 352-366
	AY	Boyd Fowler, Abbas El Gamal, David X.D. Yang "A CMOS Area Image Sensor with Pixel-Level A/D Conversion"; 1994 IEEE International Solid-State Conference, pages 226-227.
	AZ	M. Degrauwe, E. Vittoz, I. Verbauwhede, "A Micropower CMOS-Instrumentation Amplifier" IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 3, June 1985, pages 805-807
Examiner		Date Considered
<p>*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>		